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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Marquette John Anderson, Et al.      Art Unit: TBD  
Serial No.: 09/778,495      Examiner: TBD  
Filed: 02/07/2001      Docket No.: TI-30831  
For: Multi-Processor System Verification Circuitry

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INFORMATION DISCLOSURE STATEMENT

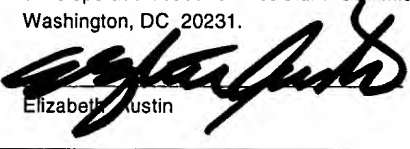
March 30, 2001

Assistant Commissioner For Patents  
Washington, DC 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(A)

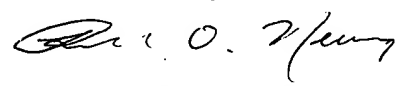
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231.

 3/30/2001  
Elizabeth Austin      Date

In response to the Search Report received from the European Patent Office (a copy of which is enclosed), Applicant wishes to bring to the attention of the U.S. Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the cited references are enclosed.

If required, please charge the necessary \$240 fee to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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